Firmware Program Release Notes

Overview

Program Type (General or Customized)	General
Applicable Receiving Card	A7sPlus
PCB Version	S12C_B1

Chip List

Program Package	Driver IC	Decoding Chip	Note
DATA_A7s Plus_V4.9. 1.0	*Common:* *MBI series:* *MBI5124 (excluding MBI5124DPWM), MBI502x, MBI5034—MBI5039* *MY series:* *MY9862* *ICN series:* *ICN2038S, ICN2047, DP5125H (ICN2038S), ICND1069, ICND2055S* *SM series:* *SM16207, SM16237* *DP series:* *DP3246* *Others:* *Common chips, RT5938ss_Common* *ICN series:* *ICN2055 (ICN1069) / ICN2065 / ICN2069 (FM6565), ICN1065, ICN1065S (ICN2065), ICN3065, ICN2153s, ICN2055s (ICN2055), ICN3069* *MBI series:* *MBI5264, MBI5268* *SM series:* *SM16389SF, SM16389, SM16169SH (SM16380SH), SM16380SW (SM16380SH),*	*Static, direct, 138, 5958, 5953, 595, SM5266, SM5366, SM5368, DP32020, DP32019, D7266, HX6158H, FM7519, CFD2138s* *ICN2018 / ICN2019 / ICN3018, RT5958 / RT5988 / RT5960 / RT5990 / RT5992 / RT5929*	*The 16169SH series does not support special modes.*

	SM16169SW (SM16380SH), SM16386 (SM16380SH) *DP series:* *DP3264, DP3265, DP3368, DP3256 (DP3264), DP3356 (DP3265), DP3364 (DP3265), DP3254, DP3252 (DP3254), DP3153 (DP3254), DP3269s* *CFD series:* *CFD555A, CFD555B, CFD535A (CFD555B), C8385, CFD955B* *HX:* *HX8863, HX8864, HX8865, HX8964 (HX8864+Register group count)* *FM series:* *FM6864 (MBI5264), FM6373*		
DATA_A7s Plus_V4.9.1.3	*MY series:* *MY9266, MY9269, MY9366* *MBI series:* *MBI5030, MBI5031, MBI5041 (B), MBI5043, MBI505x, MBI5252, MBI5353, MBI5153 (FM6153), MBI5353B, MBI5253B, MBI5754B* *ICN series:* *ICN2153 (FM6353), ICN2159, ICN2263* *SM series:* *SM16169, SM16380, SM16388, SM16369, SM16359* *CFD series:* *CFD435A, CFD455A, CFD335A* *CS series: CS2033, CS2066* *Others:* *TLC5958, TLC59581, SCL8060*	*Static, direct, 138, 5958, 5953, 595, SM5266, SM5366, SM5368, DP32020, DP32019, D7266, HX6158H, FM7519, CFD2138s,* *ICN2018 / ICN2019 / ICN3018, RT5958 / RT5988 / RT5960 / RT5990 / RT5992 / RT5929*	*ICN2263 does not support dead pixel removal.*
DATA_A7s Plus_V4.9.1.4	*LS-PWM:* *LS9935, LS9935B, LS9936* *LS-Common:* *LS9917, LS9919, LS9920, LS9926, LS9928, LS9929, LS9930, LS9931, LS9961*	*Direct, 7258/2012, 2013, 9739, 9737, 9736, 9735, 5958, 2018, 5266, 5366, 7266, 32019, 32020, 5368, 6158, 9708, 9716*	

Features

Supported Features	Note
Max load capacity: 512x512 (PWM, Shixin PWM IC) 512x384 (Common, Shixin common IC)	
Up to 1/128 scan	
Up to 32 groups of parallel data; 64 groups of serial data (expandable to 128 groups of serial data)	Refer to logical load capacity. Support 8, 16, or 32 groups of parallel data. Support 16, 32, or 64 groups of serial data. Support dual clock extension by default.
Pixel-level brightness calibration	
Quick seam correction	Support displaying test pattern when there is no signal source.
Support 3D	Load capacity reduced by half.
Bit error detection	
Individual gamma adjustment for RGB	
90° image rotation	
Display of 3-color 16-point serial input	
Stable uploading of calibration coefficients	Calibration acceleration supported
Mapping 1.1	
Temperature & voltage monitoring	
Cabinet LCD	Only 5-Pin LCD is supported
Firmware program readback	
Configuration parameter readback	
Loop backup	
Dual program backup	
No rectangle restriction	Work with specific sending cards.

Others

The main improvements in the A7s Plus v4.9 include:

1. Seamless switching: In backup mode, the system can perform primary-backup switching without any noticeable interruption.

2. System optimization: Improved system stability.

Change History

Change History		
Program Version	**Release Date**	**Description**
V4.9.1.0		
V4.9.0.0		

V4.9.1.0 Updates

New Features
• Improved bit error detection with a new mechanism to track the number of Ethernet cable disconnections:
• Added support for ICND1069 and ICND2055S driver ICs.
Bug Fixes
• Improved display performance for screen unplugging and re-plugging of Ethernet cables.
• Fixed compatibility issues with dual-card backup.
• Fixed occasional screen distortion with FM6373 after power cycling.
• Fixed occasional display synchronization issues when working with the MCTL660 sending card.
• Fixed lighting anomalies with DP3269S and DP3246.

V4.9.0.0 Updates

New Features
• Seamless switching: In backup mode, the system can perform primary-backup switching without any noticeable interruption.
• System optimization: Improved system stability.
Improvements
• Optimized the power-on sequence for PWM and common ICs.
• Improved the gradual power-on functionality.
• Improved the 18bit display quality for active-low chips.
• Improved the redundant backup switching performance.
• Improved dual-card backup functionality and primary-backup switching performance.
• Improved the decoding timing for DP32020.

- Increased the maximum number of module flash to 64.

- Improved dead pixel removal with an added toggle feature, and enhanced display performance after power cycling.

- Improved 18bit+ display quality.

- Resolved flash operation issues caused by hardware interference.

- Improved certain display effects when working with COEX systems.

Bug Fixes

- Fixed display issues with IC under certain settings.

- Fixed black screen issues related to DDR.

- Fixed black screen problems in display control.

- Fixed display anomalies caused by spread spectrum functionality.