MRV328 Firmware Program Release Notes

V4.5.4.0

**Improvements:**

1. Optimized network communication to improve transmission stability.
2. Fixed the problem of abnormal bit error rate monitoring when the MRV328 works with the MCTRL4K sending card.

**Note:**

1. The FPGA program and MCU program in V4.5.4.0 must work together. Either of them cannot work with a historical version of the other program.

V4.5.3.0

**Features:**

1. Loading capacity: 256×256 pixels
2. Support for up to 1/64 scan
3. Support for up to 16 groups of parallel data
4. Voltage and temperature monitoring
5. Support for setting of a pre-stored image in receiving card
6. Pixel level brightness and chroma calibration
7. Quick adjustment of dark or bright lines
8. Bit error rate monitoring
9. Support for a 5-pin LCD module
10. 3D function
11. Configuration parameter readback
12. Firmware program readback
13. Loop backup
14. Dual backup of program

**Driver Chips:**

1. MBI series: MBI502x, MBI503x, MBI5041(B), MBI5042(B), MBI5043, MBI505x, MBI5124 (not including MBI5124DPWM), MBI5125 (not including MBI5125DPWM), MBI515x, MBI5252
2. SUM series: SUM20167, SUM2017(T), SUM2028, SUM203x, SUM213x
3. MY series: MY9266, MY9269, MY9366, MY9862, MY9868
4. ICN series: ICN2027, ICN2028, ICN2038, ICN2038S, ICN2053
5. SM series: SM16158, SM16159, SM16207, SM16227, SM16237
6. Others: TLC5958, TLC59581, SC8060, common chips

**Decoding Chips:**

1. 74HC138 chip
2. 74HC595 chip
3. RT5953 and RT5958 chips
4. SM5266 chip
5. ICN2012 and SM5366 chips